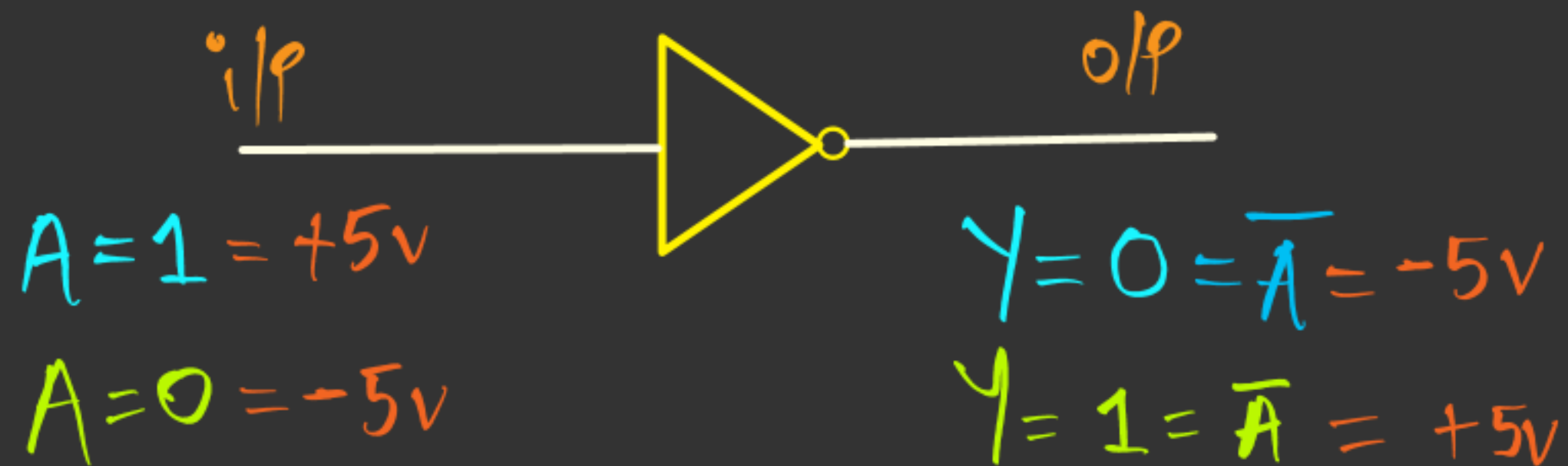


@ Basic Gates:-

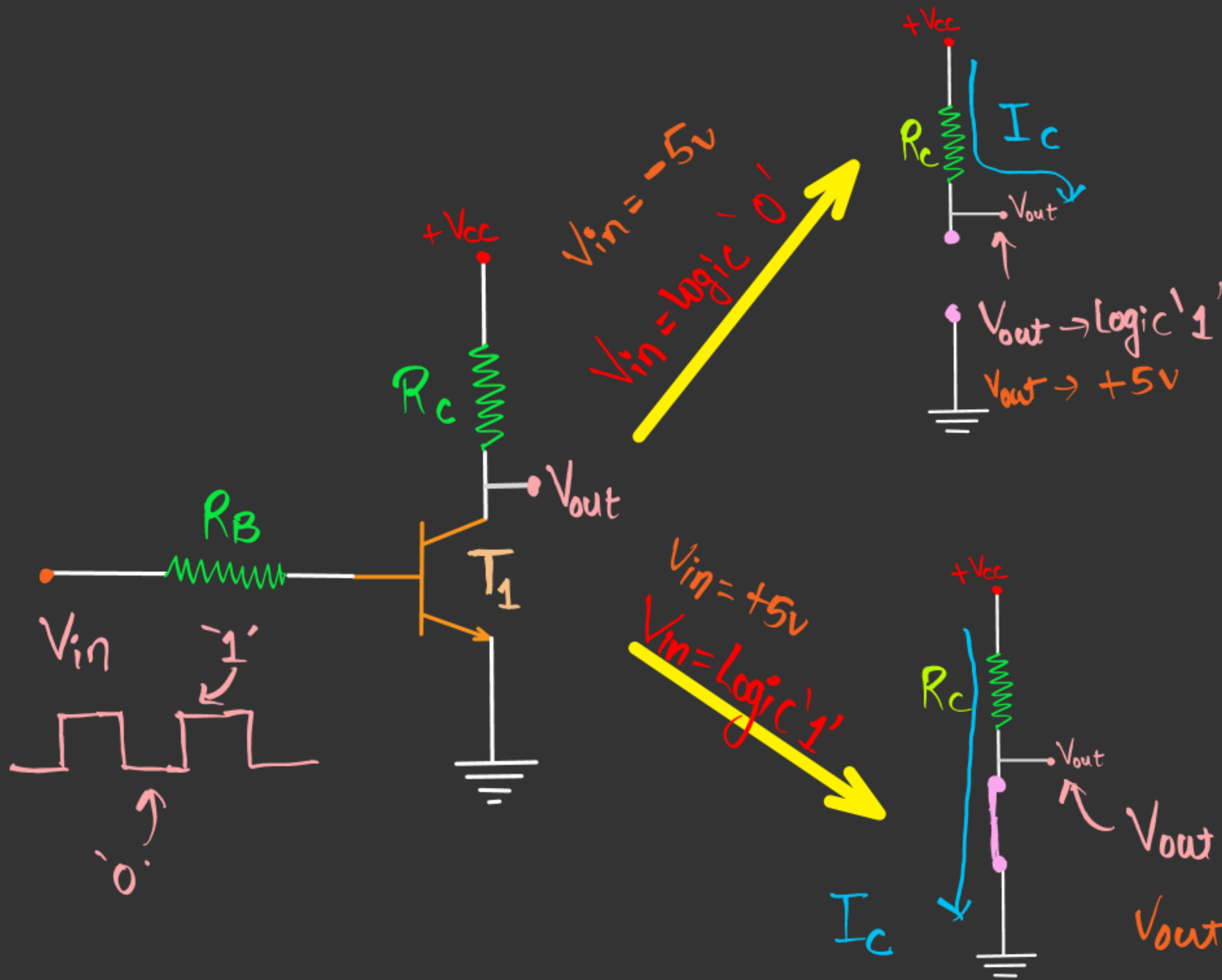
1) NOT Gate:-

NOT Gate performs inversion operation
↓
opposite

Symbol:-



A	$Y = \overline{A}$
1	0
0	1



V_{in}	Transistor T_1	V_{out}
0	Cutoff reg. (open switch)	1
1	Satura ⁿ reg. (closed switch)	0

